

Professor Samary Baranov. More than thirty years experience in the electronics industry, culminating in being the project manager of the first VLSI circuit in the former Soviet Union. In 1975, joined the Computer Engineering Dept. of the Institute of Precise Mechanics and Optics in St. Petersburg as a professor. Hundreds of his students, including about 80 with Masters Degrees and 20 with Ph.D. degrees, work successfully in Canada, Germany, Israel, Russia and USA for well known IT companies, such as National Semiconductors, Motorola, Intel, IBM, etc.

In 1991, joined the Dept. of Mathematics and Computer Science at the University of Beer-Sheva (Israel) as a professor and in 1994 became a professor in the Computer Science Dept. and the Head of the Center for VLSI Design at the Holon Institute of Technology (HIT). Lectured at the Electrical and Computer Engineering Dept. of the University in Beer Sheva and the Tel Aviv University. At the same time, worked as a consultant for several high-tech companies in Israel, including National Semiconductors, Fortress, and M-Systems. In February 2001, founded in Toronto the North American Institute of Computer Systems (NAICS) – the first training center in Canada to offer an advanced and intensive 250 hour post-graduate curriculum “Electronic Hardware Design” devoted to teaching Design Methodology, Hardware Description Languages (VHDL and Verilog), EDA Tools, ASIC and FPGA Design, Verification of Digital Systems with numerous projects including RISC processor design (final project) and FFT processor design (internship project). More than 250 students (bachelors, masters and PhDs) successfully finished this course in 2001 – 2003. Founded Center for VLSI Design in HIT. Teaches main courses in VLSI design in HIT and in Bar Ilan University.

Developed design methodology for high-level synthesis of digital systems with hardware description languages. Elaborated methods, algorithms and programs (tools) supporting this design methodology. Introduced these tools into industrial design projects and educational process.

Developed EDA (Electronic Design Automation) tool Abelite that reduces the circuits by as much as 50%, compared with results of such the best USA EDA tools as Leonardo, Synopsys, Cadence, Xilinx, Altera, and Synplicity and yet the run times of these other CAD systems exceed that of Abelite by more than a factor of 100. In 2006. got a grant from Intel. Intel didn't pretend to his IP and gave this grant only for a possibility to use some his programs in its designs.

Author of ten books, and more than 70 papers in Russian, French and English. His latest book "Logic Synthesis for Control Automata" was published by Kluwer Academic Publishers. The new book “Logic and System Design of Digital System” will be published in 2007.